

Electronic Acknowledgement Receipt

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Title of Invention:	Low pin count docking architecture for a trusted platform
First Named Inventor/Applicant Name:	Sundeeep M. Bajikar
Customer Number:	8791
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	NPL Documents	Crawford.pdf	543835	no	8

Warnings:

Information:					
2	NPL Documents	DAVIDA.PDF	379657	no	8
Warnings:					
Information:					
3	NPL Documents	Fabry.pdf	858782	no	10
Warnings:					
Information:					
4	NPL Documents	Frieder.pdf	627880	no	8
Warnings:					
Information:					
5	NPL Documents	GOLDBERG.PDF	1533740	no	14
Warnings:					
Information:					
6	NPL Documents	GONG.PDF	694288	no	11
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7	NPL Documents	GUM.PDF	1008211	no	15
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Information:					
8	NPL Documents	Hall.pdf	1453016	no	10
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9	NPL Documents	HEINRICH.PDF	1394453	no	56
Warnings:					
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11	NPL Documents	IBMinfo.PDF	101111	no	4
Warnings:					
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12	NPL Documents	IBMibmTP.pdf	249856	no	6
Warnings:					
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13	NPL Documents	INTELia32.PDF	1910982	no	43
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14	NPL Documents	IntellA-64system.pdf	1717175	no	47
Warnings:					
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15	NPL Documents	Intel_82802AB.pdf	695366	no	18
Warnings:					
Information:					
16	NPL Documents	IntellA64arch.pdf	643745	no	17
Warnings:					
Information:					
17	NPL Documents	INTEL386.PDF	2657448	no	56
Warnings:					
Information:					
18	NPL Documents	KARGER.PDF	1035296	no	18
Warnings:					
Information:					
19	NPL Documents	KASHIWAGI.PDF	454565	no	10
Warnings:					

Information:					
20	Information Disclosure Statement (IDS) Filed	P16632ids4.pdf	115703	no	4
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